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Date: November 20, 2003

By: Sandy Reisman

Sandy Reisman

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: DAN DAEWEON CHEONG

APPLICATION NO.: 10/606,992

FILED: JUNE 25, 2003

FOR: **MULTIPLE SOURCE DEPOSITION
PROCESS**

EXAMINER: UNKNOWN

ART UNIT: 1762

CONF. NO: 4507

Request For Corrected Filing Receipt

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Sir:

Attached is a copy of the official Filing Receipt received from the U.S. Patent and Trademark Office in the above-identified application, for which issuance of a corrected Filing Receipt is respectfully requested.

There is an error with respect to the following data, which was incorrectly entered:

The filing date for this application should be --06/25/2003--. This application was transmitted as Express Mail No. EV343588264US. See the enclosed copy of page 1 of the application showing Express Mail No. EV343588264US. Also enclosed are copies of Express Mail Label No. EV343588264US showing a "Date In" of "6 25 03" and the United States Postal Service receipt showing that Express Mail package no. EV343588264US was received on "06/25/03" at "04:45PM". Finally, the enclosed tracking information from the USPS confirms that Express Mail package no.

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The correction to be made has been marked in red on the enclosed copy of the Filing Receipt.

The correction is not due to any errors by applicant, and therefore no fee is due.

Respectfully submitted,
Perkins Coie LLP



Edward S. Hotchkiss
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Applicant(s)

Dan Daeweon Cheong, City of Mississauga, CANADA;

Domestic Priority data as claimed by applicant

This application is a DIV of 09/747,315 12/22/2000 PAT 6,610,352

Foreign Applications

If Required, Foreign Filing License Granted: 11/04/2003

Projected Publication Date: 02/12/2004

Non-Publication Request: No

Early Publication Request: No

Title

Multiple source deposition process

Preliminary Class

TITLE**MULTIPLE SOURCE DEPOSITION PROCESS**5 **Field of the Invention**

The present invention relates to a method for the deposition of a ternary, quaternary or similar composition, especially a phosphor, in which components of the composition are located on different sources. In particular, the compositions are thioaluminates, thiogallates or thioindates of Group IIA
10 and Group IIB elements, and the sulphides that form such compounds are located on the different sources. A preferred method of deposition is electron beam vaporization. In particular aspects, the present invention relates to a method of improving the luminance and emission spectrum of phosphor materials, especially those used for full colour ac electroluminescent displays
15 employing thick film dielectric layers with a high dielectric constant.

Background to the Invention

Thin film electroluminescent (TFEL) displays are known and are typically fabricated on glass substrates. However, electroluminescent
20 displays with thin film phosphors employing thick film dielectric layers fabricated on ceramic substrates, as exemplified by U.S. Patent 5 432 015, provide greater luminance and superior reliability.

Thick film dielectric structures provide superior resistance to dielectric breakdown, as well as a reduced operating voltage. When deposited on a
25 ceramic substrate, the thick film dielectric structure will withstand higher processing temperatures than TFEL devices on glass substrates. The increased tolerance to higher temperatures facilitates annealing of the phosphor films at higher temperatures, to improve luminosity. However, even with the enhanced luminosity that is obtained, electroluminescent displays
30 employing a thick film dielectric layer have not achieved the phosphor luminance and colour coordinates needed to be fully competitive with cathode ray tube (CRT) displays. Moreover, recent trends in CRT specifications are to higher luminance and higher colour temperature. Some improvement has been realized by increasing the operating voltage of electroluminescent



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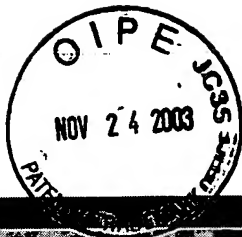
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